

FIG. 1



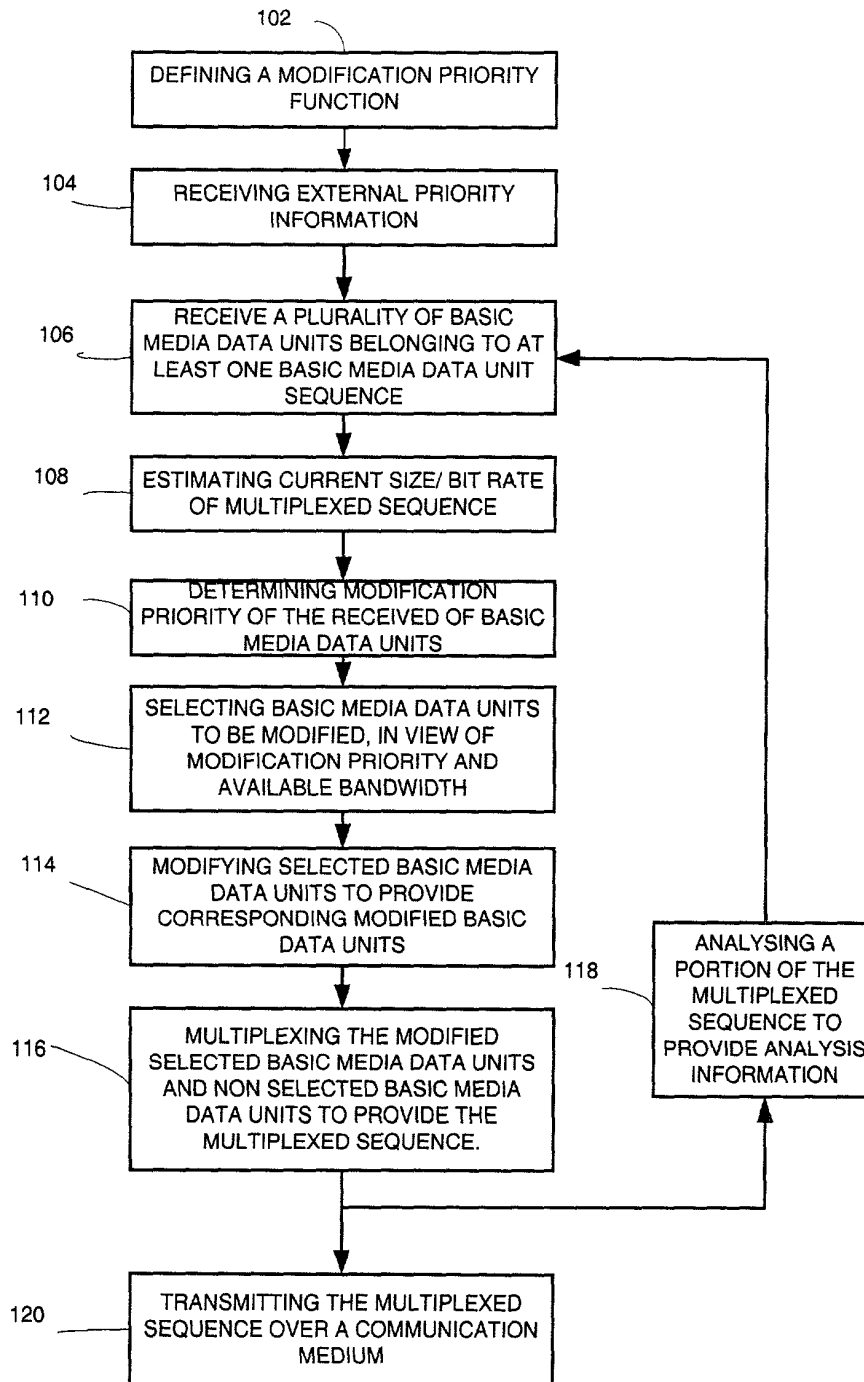


FIG. 3

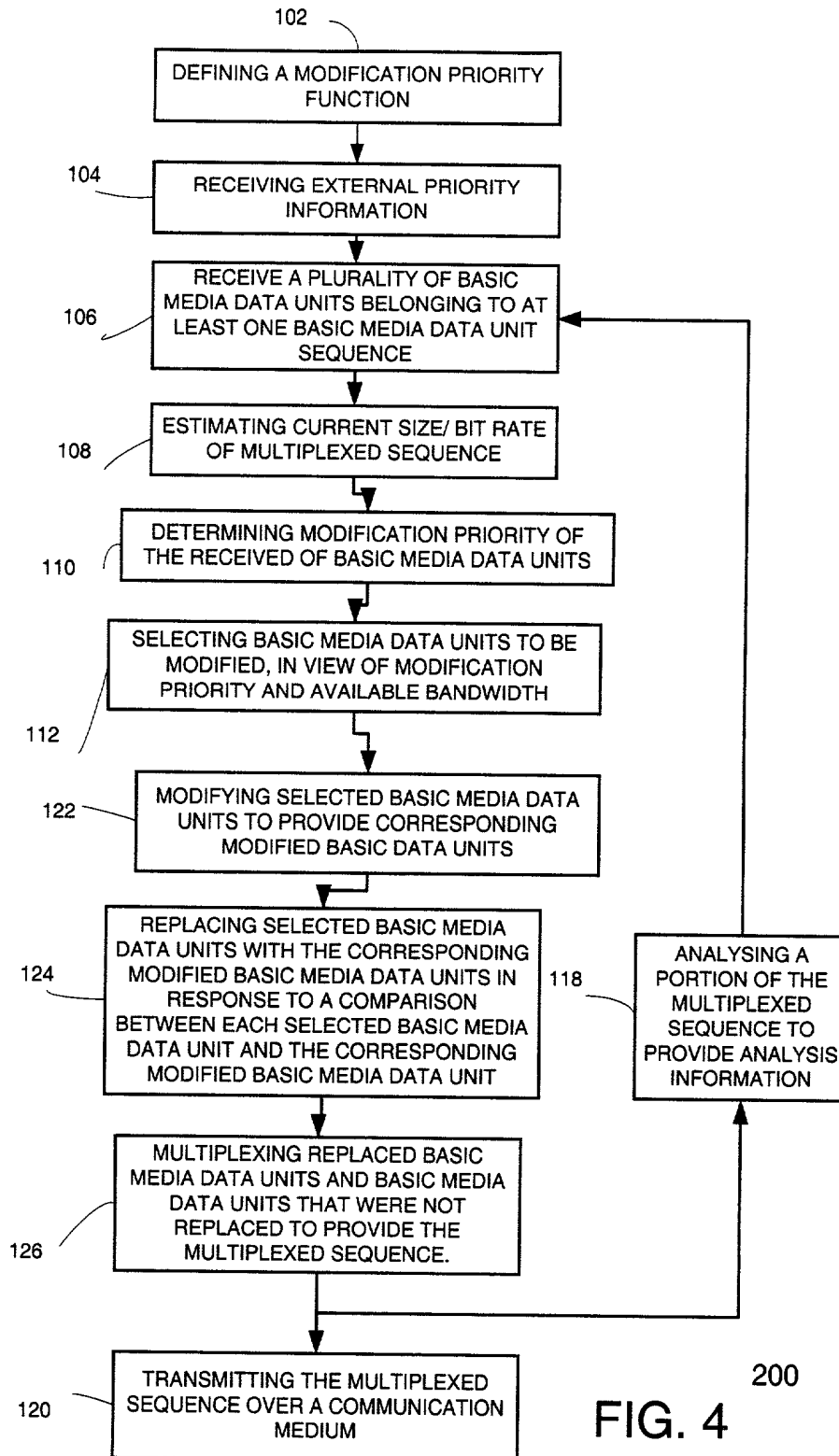


FIG. 4

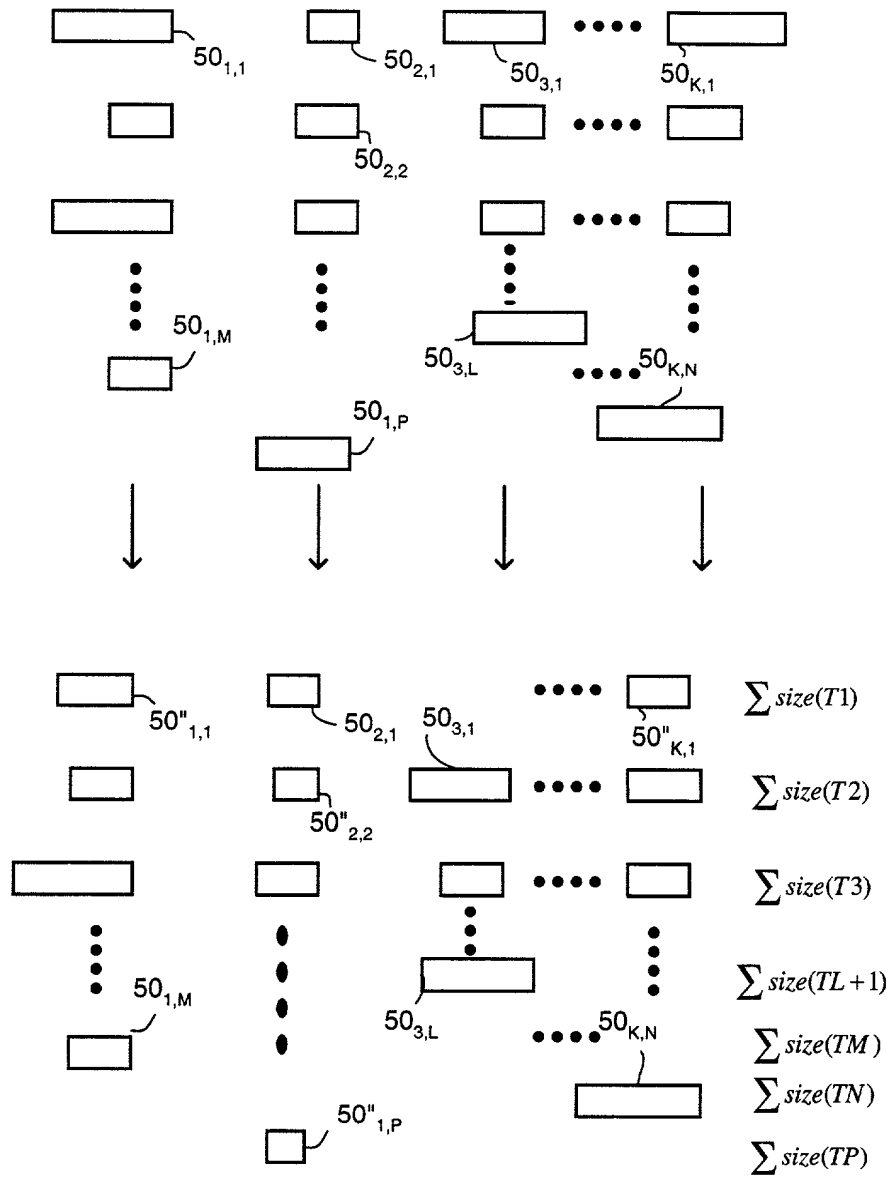


FIG. 5

FIG. 6 is a block diagram of a system 200 for simulating a packetizing process.

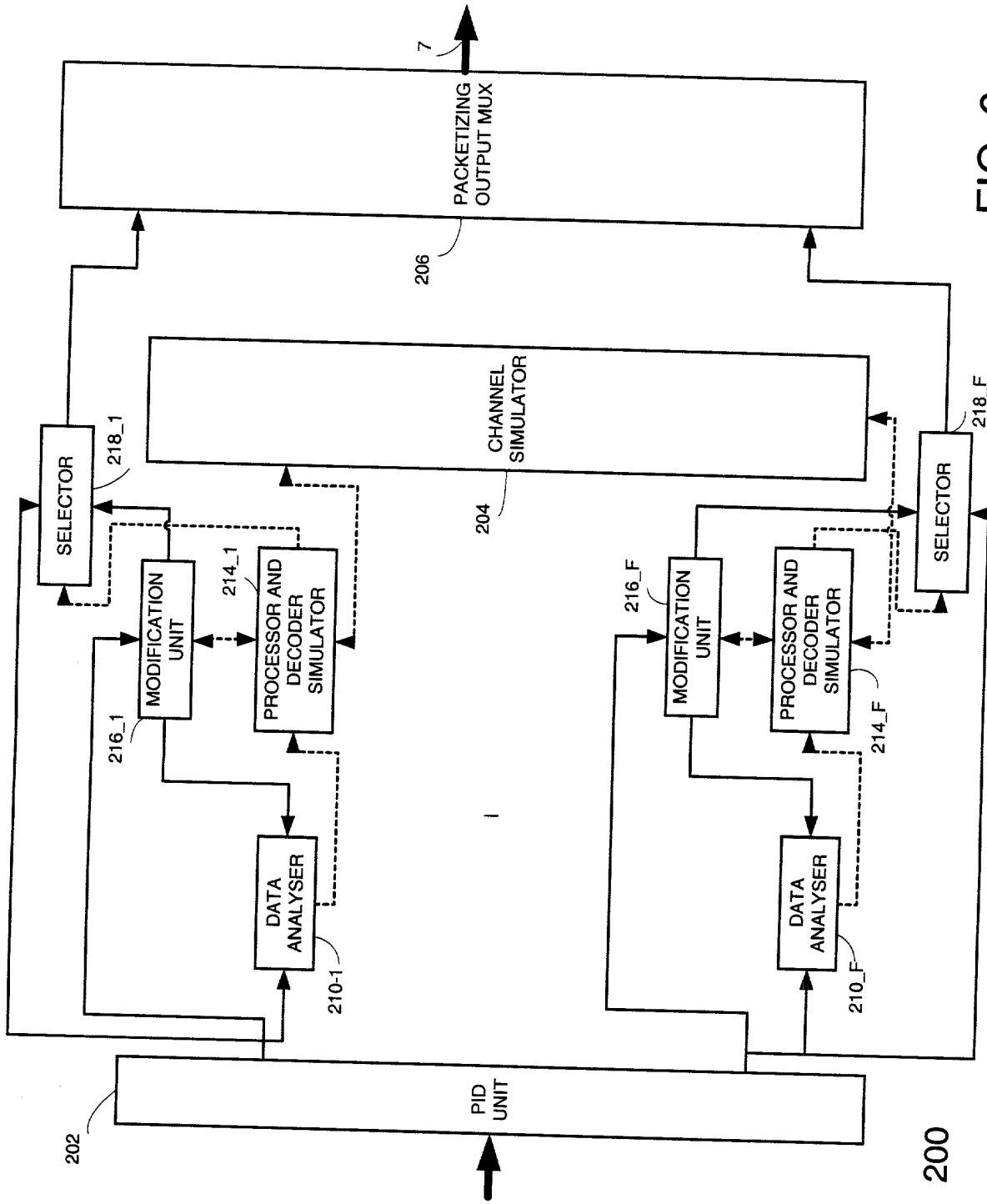


FIG. 6